FIG. 1

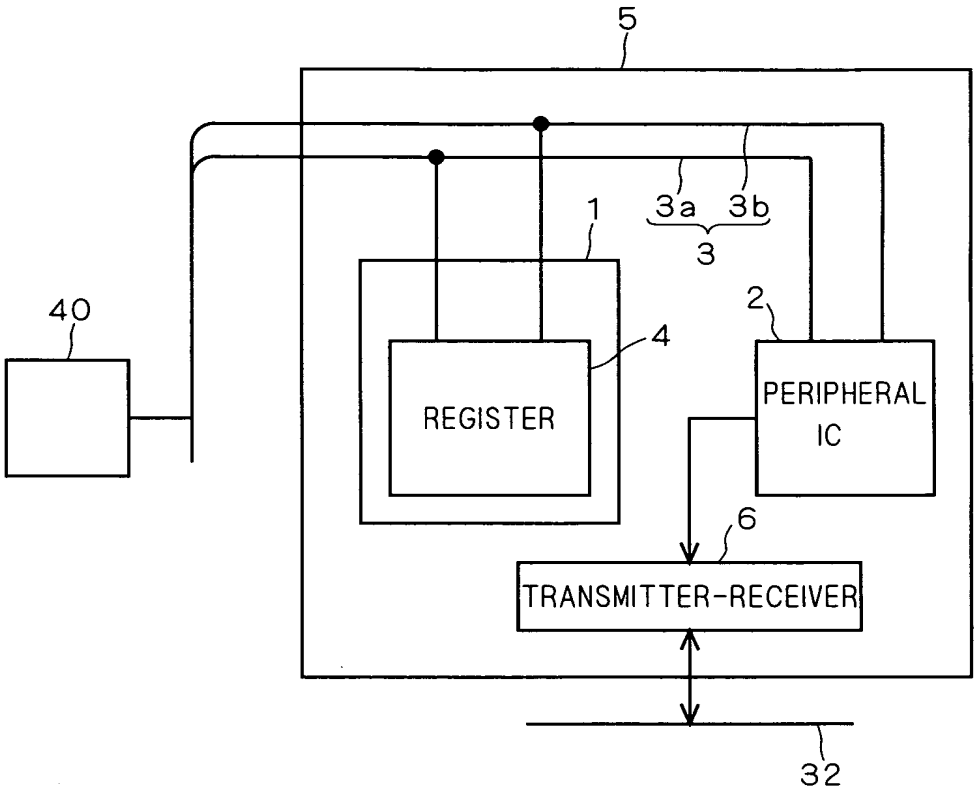

5
3a
3b
3
1
2
40
4
REGISTER
PERIPHERAL IC
6
TRANSMITTER-RECEIVER
32

FIG. 2

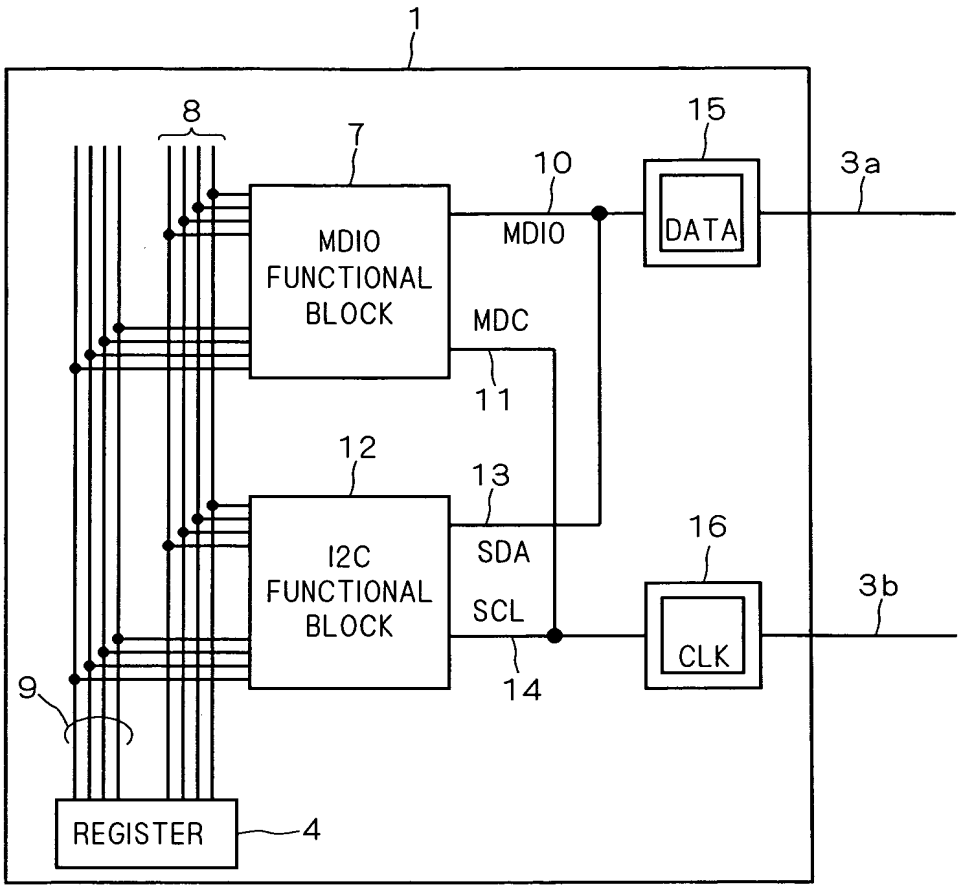

1
8
7
15
10
3a
MDIO
FUNCTIONAL
BLOCK
MDIO
DATA
MDC
11
12
13
16
I2C
FUNCTIONAL
BLOCK
SDA
SCL
3b
CLK
9
14
REGISTER
4

FIG. 3

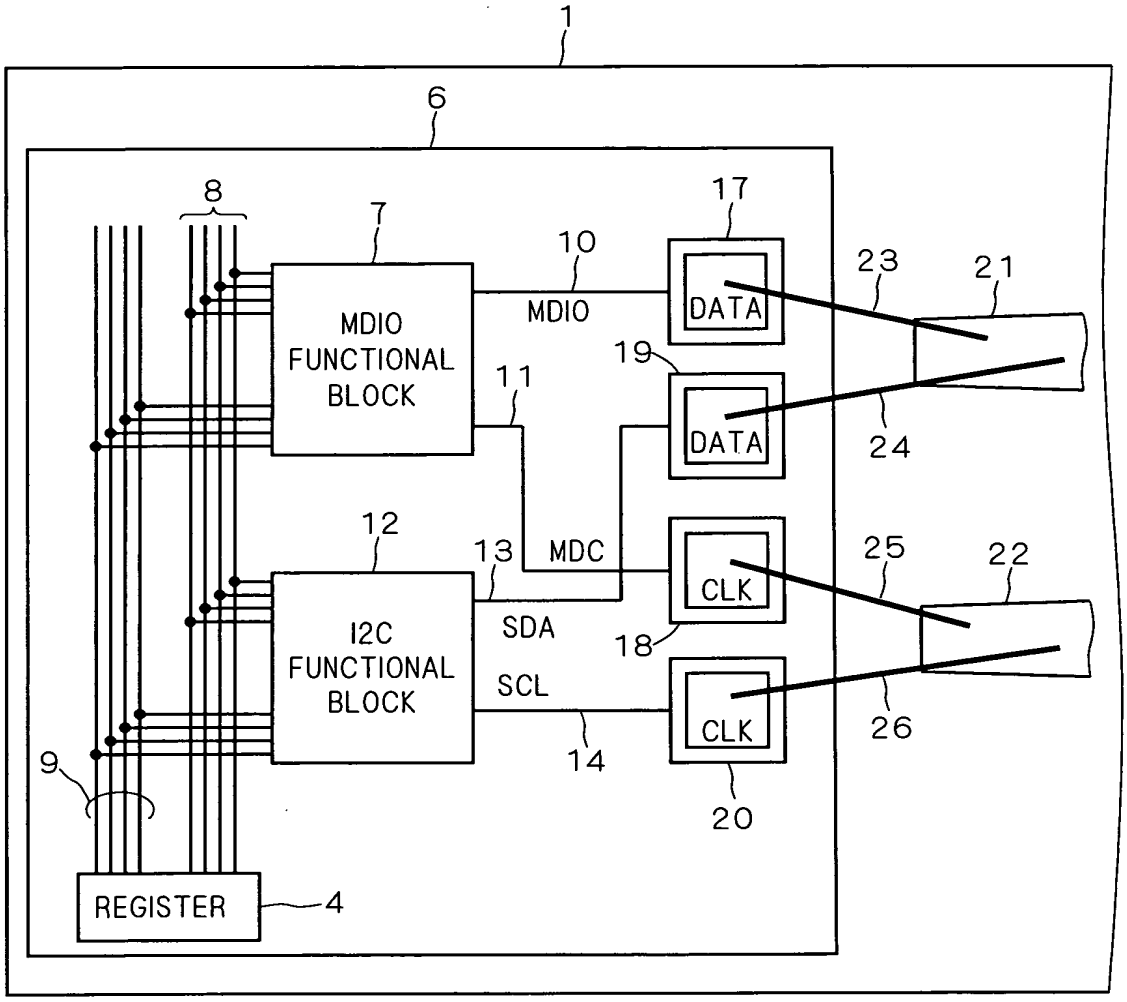
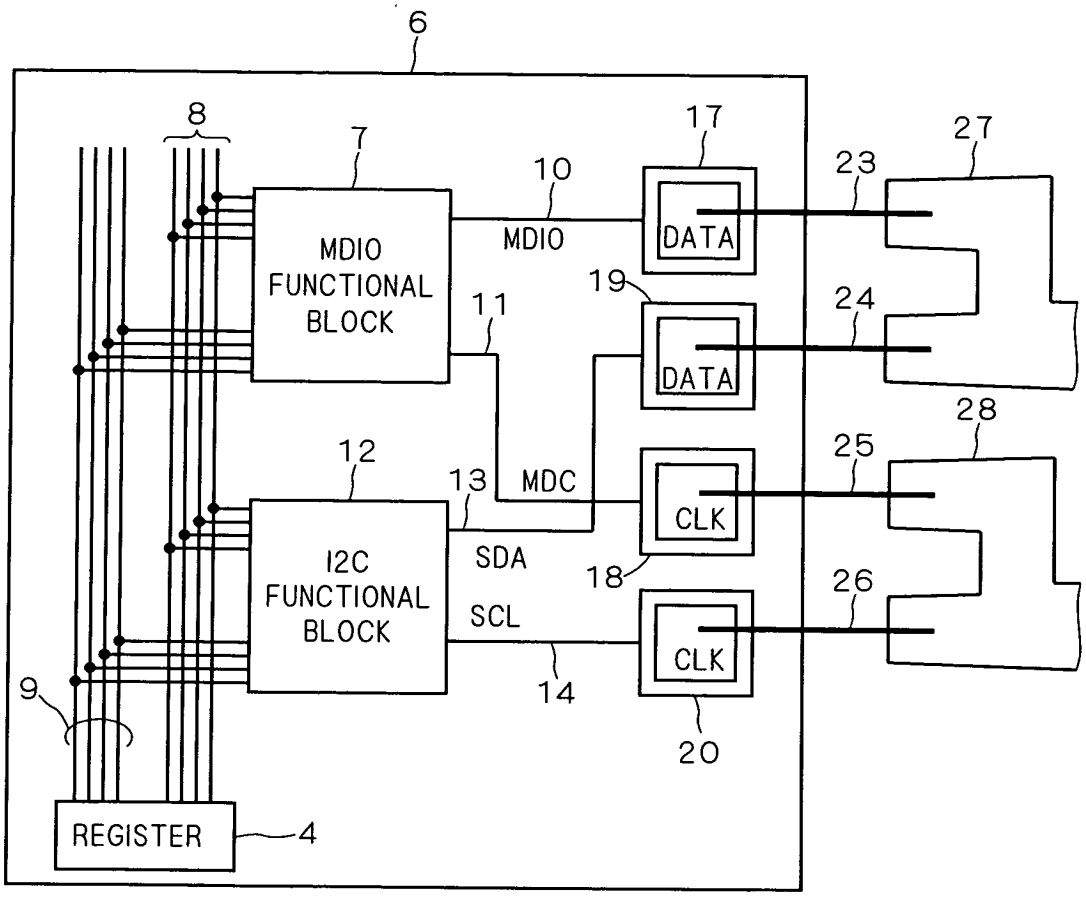

1
6
8
7
17
10
MDIO
FUNCTIONAL
BLOCK
MDIO
DATA
23
21
19
11
DATA
24
12
13
MDC
CLK
25
22
I2C
FUNCTIONAL
BLOCK
SDA
18
SCL
CLK
26
9
14
20
REGISTER
4

FIG. 4


6
8
7
10
17
23
27
MDIO
FUNCTIONAL
BLOCK
MDIO
DATA
19
11
24
DATA
28
12
13
MDC
25
CLK
SDA
18
I2C
FUNCTIONAL
BLOCK
SCL
26
CLK
9
14
20
REGISTER
4